

27 March 2024

Boolean Satisfiability (SAT)

Plan

* Recall CNF-SAT

* Announcements

* Reductions TO SAT

↳ $\text{INDSET} \leq_P \text{CNF-SAT}$

↳ $\text{CIRCUIT-SAT} \leq_P \text{3SAT}$

Boolean Satisfiability

Given: boolean formula φ in Conjunctive Normal Form

$$\text{CNF } \varphi = (x_1 \vee \neg x_2 \vee x_5) \wedge (x_2 \vee x_3 \vee x_7 \vee \neg x_6) \\ \wedge \dots \wedge (\neg x_1 \vee \neg x_{100})$$

Question.

Does there exist an truth assignment to (x_1, \dots, x_n) that satisfies φ ?

Boolean Satisfiability

Given: boolean formula φ in Conjunctive Normal Form

$$\text{CNF } \varphi = (x_1 \vee \neg x_2 \vee x_5) \wedge (x_2 \vee x_3 \vee x_7 \vee \neg x_6) \\ \wedge \dots \wedge (\underbrace{\neg x_1 \vee \neg x_{100}}_{\text{clauses}})$$

literals
(variable or negation)

Question

Does there exist an truth assignment to (x_1, \dots, x_n) that satisfies φ ?

$\vec{a} = (1, 0, 0, \dots, 1)$

$\varphi(\vec{a}) = T$

Why study SAT?

Cook-Levin Theorem.

SAT is NP-Complete.

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Cook-Levin Theorem.

SAT is NP-Complete.

\hookrightarrow SAT is in NP.

Why study SAT?

Cook-Levin Theorem. SAT is NP-Complete.

↳ SAT is in NP.

Poly-time verifier for SAT.

$V(\varphi, \vec{a})$.

For each clause in φ

if \vec{a} does not satisfy clause

Return \perp

Return \checkmark

Why study SAT?

Cook-Levin Theorem. SAT is NP-Complete.

↳ SAT is in NP.

↳ SAT is NP-Hard.

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Every efficiently verifiable problem reduces
to SAT!

Why study SAT?

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↳ SAT is in NP.

↳ SAT is NP-Hard.



Every efficiently verifiable problem reduces
to SAT!

Solving SAT is hard!

Solving SAT is powerful!

Announcements

- * HW 7 due Thurs, 11:59 pm
 - ↳ See Ed Post about update to Q3 (makes problem easier)
- * Prelim Conflict Survey
 - ↳ Link on Ed
- * Prelim #2 Review Session
 - ↳ April 9, 7-9 pm, Gates G01.

Every efficiently verifiable problem reduces

Solving SAT is hard! to SAT!

Solving SAT is powerful!



Every efficiently verifiable problem reduces
Solving SAT is hard! to SAT!

Solving SAT is powerful!

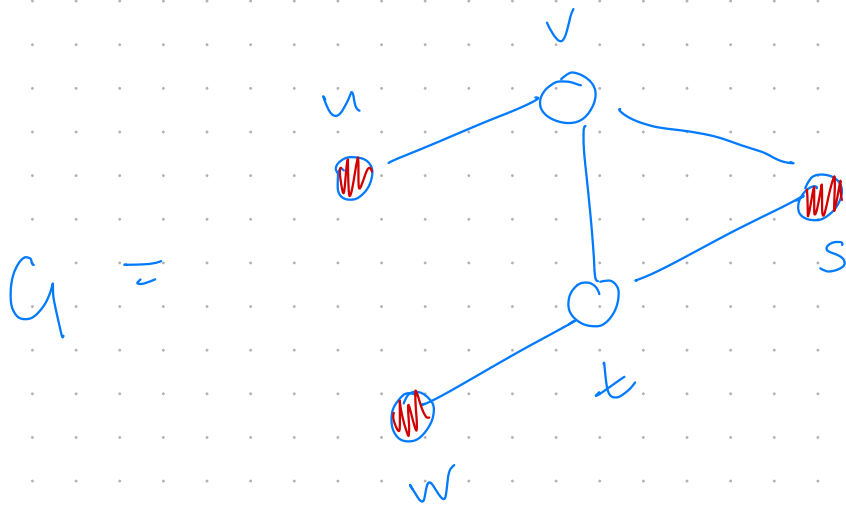
- ↳ Practical Algorithm design paradigm:
- Reduce problem to SAT
 - Use optimized SAT SOLVER
to solve the problem

INDEPENDENT SET REDUCES TO CNF-SAT

↳ Given: Graph G , parameter k $|S| \geq k$

Find: A subset $S \subseteq V$ of vertices

such that no two vertices $u, v \in S$
share an edge $(u, v) \in E$



reduction
→

φ_G

φ_G satisfiable

↔

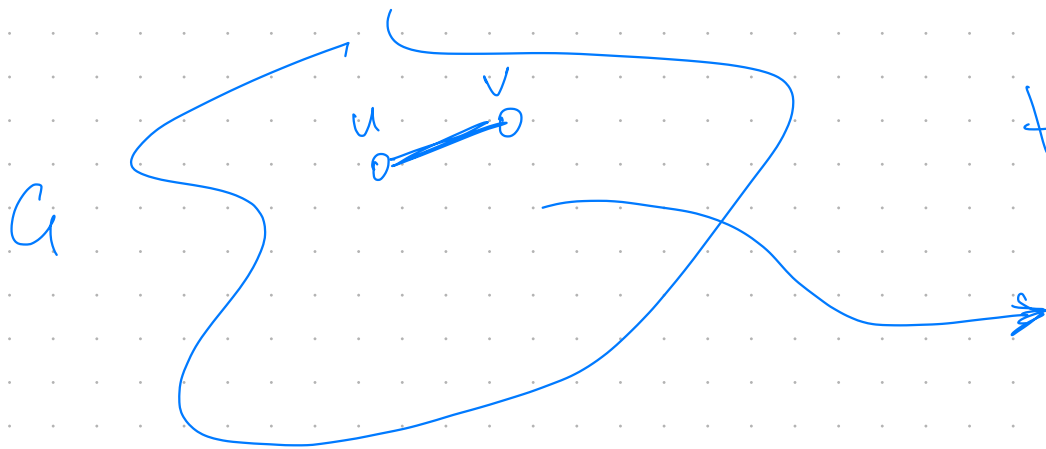
G has $\text{INDSET} \geq k$

INDEPENDENT SET REDUCES TO CNF-SAT

↳ Given: Graph G , parameter k

Find: A subset $S \subseteq V$ of vertices $|S| \geq k$

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$$\forall (u, v) \in E$$

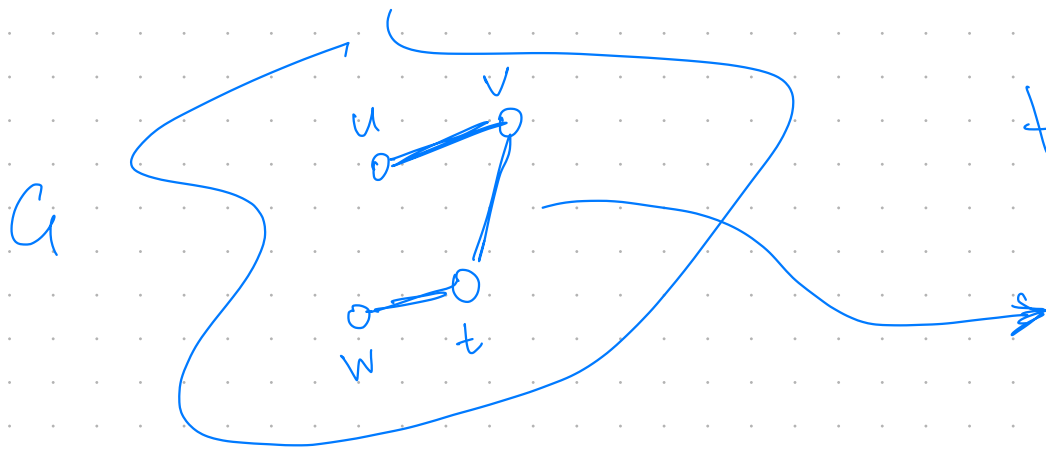
$$\neg(u \in S) \vee \neg(v \in S)$$

INDEPENDENT SET REDUCES TO CNF-SAT

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$$\forall (u, v) \in E$$

$$\underbrace{\neg(u \in S)}_{x_u} \vee \underbrace{\neg(v \in S)}_{x_v}$$

$$(\neg x_u \vee \neg x_v) \wedge (\neg x_v \vee \neg x_t) \wedge (\neg x_w \vee \neg x_t) \wedge \dots \quad \forall e \in E$$

INDEPENDENT SET REDUCES TO CNF-SAT

↳ Given: Graph G , parameter k

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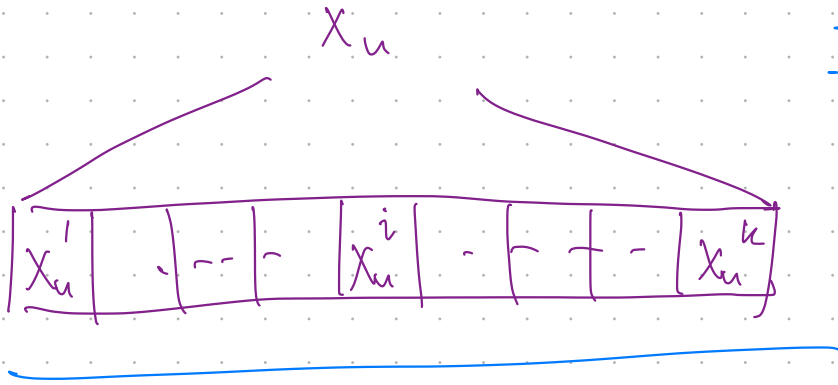
Need some mechanism to count to k !

Otherwise $S = \emptyset$, i.e. $\vec{a} = \vec{0}$ satisfies $\varphi|_G$

$$(\neg x_u \vee \neg x_v) \wedge (\neg x_v \vee \neg x_t) \wedge (\neg x_w \vee \neg x_z) \wedge \dots \quad \forall e \in E$$

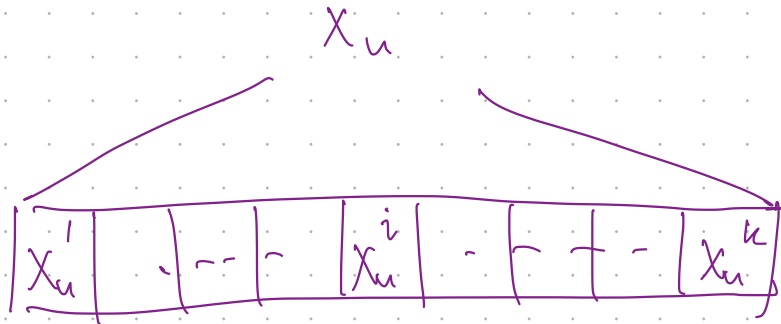
$X_u^i = T \iff u$ is the i^{th} vertex in S

Ind Set instance G, k



k boolean variable
per vertex

$X_u^i = T \iff u$ is the i^{th} vertex in S



k boolean variable
per vertex

u is "in" S at most once



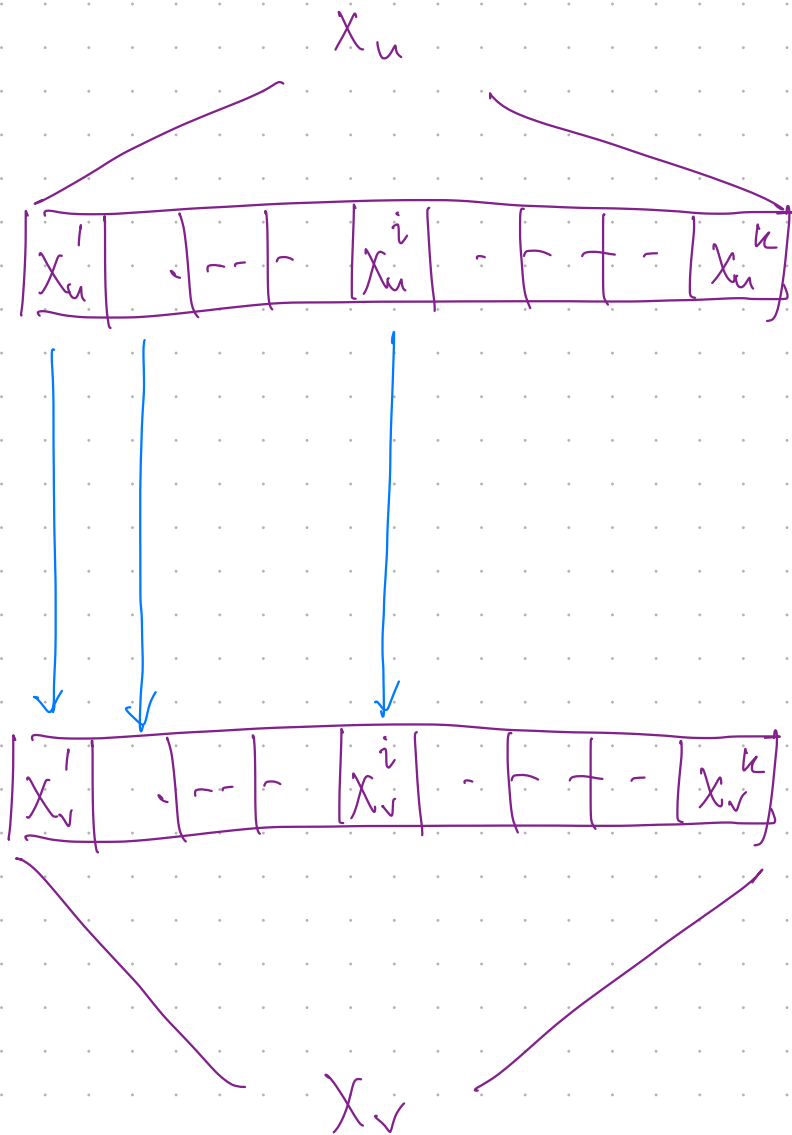
For every $u \in V$ $1 \leq i < j \leq k$

$$(\neg X_u^i \vee \neg X_u^j)$$

u cannot be i^{th} and j^{th} vertex of S

$X_u^i = T \iff u$ is the i^{th} vertex in S

Exactly 1 vertex
must be the i^{th} vertex

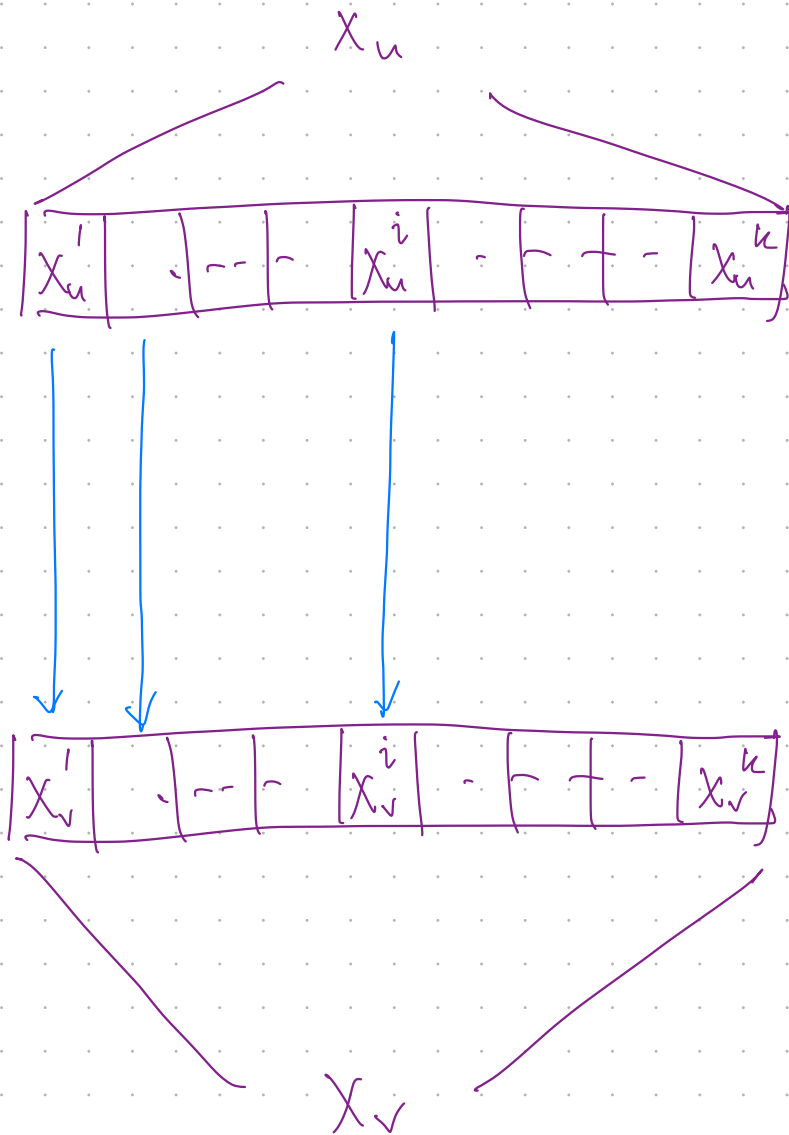


$X_u^i = T \iff u$ is the i^{th} vertex in S

Exactly 1 vertex
must be the i^{th} vertex

At least 1.

$(X_u^i \vee X_v^i \vee X_w^i \vee \dots \vee \forall v \in V)$



$X_u^i = T \iff u$ is the i^{th} vertex in S

(2)

Exactly 1 vertex must be the i^{th} vertex

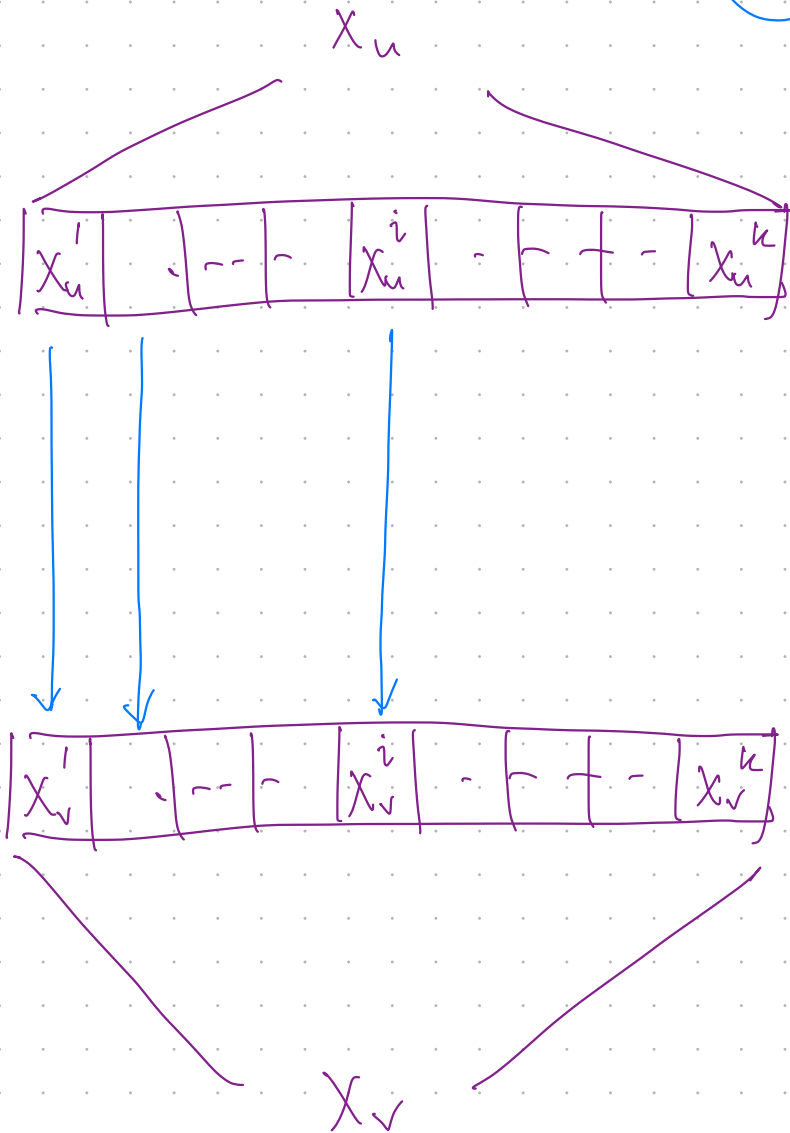
At least 1.

(2a) $(X_u^i \vee X_v^i \vee X_w^i \vee \dots \vee \forall v \in V)$

At most 1

$\forall u, v \in V$

$(\neg X_u^i \vee \neg X_v^i)$



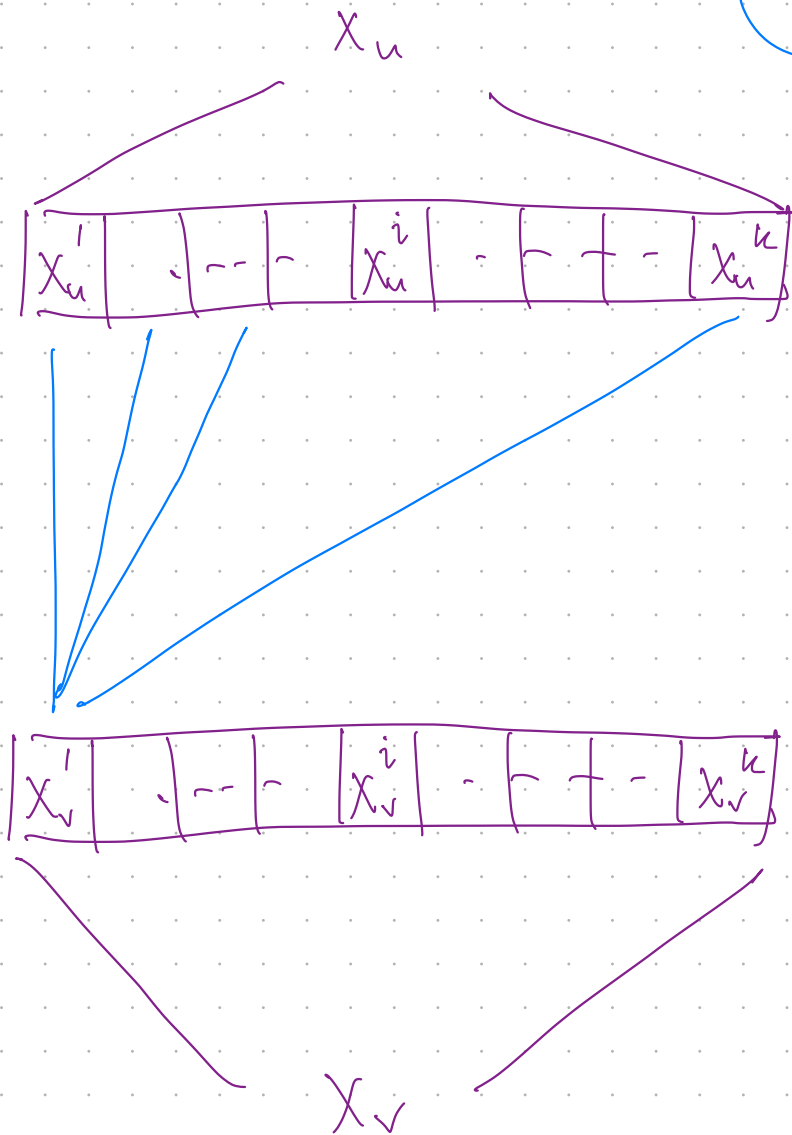
(2b)

$X_u^i = T \iff u$ is the i^{th} vertex in S

③

IND SET constraints

No vertices in S share an edge



$\forall u, v \in E$
 $\forall i, j \in \{1, \dots, k\}$

$$(\neg X_u^i \vee \neg X_v^j)$$

$$G = (V, E), \quad K$$



$$\varphi_{G,K} = \bigwedge_{u \in V} (\neg x_u^1 \vee \neg x_u^2) \wedge (\neg x_u^1 \vee \neg x_u^3) \wedge \dots \wedge (\neg x_u^{k-1} \vee \neg x_u^k) \quad (1)$$

$$\wedge (x_u^1 \vee x_v^1 \vee \dots \vee x_w^1) \wedge (x_u^2 \vee x_v^2 \vee \dots \vee x_w^2) \wedge \dots \wedge (x_u^k \vee x_v^k \vee \dots \vee x_w^k) \quad (2a)$$

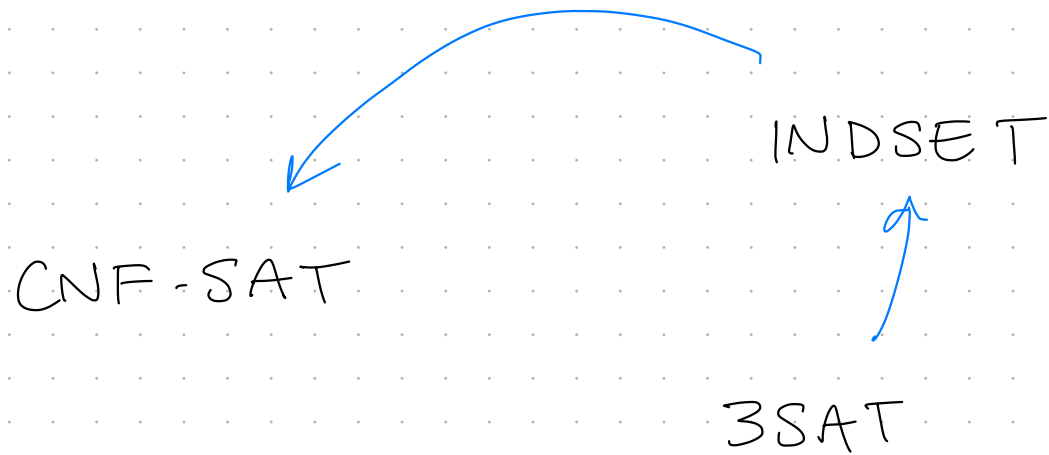
$$\wedge (\neg x_u^1 \vee \neg x_v^1) \wedge \dots \wedge (\neg x_v^1 \vee \neg x_w^1) \wedge \dots \wedge (\neg x_u^k \vee \neg x_v^k) \wedge \dots \wedge (\neg x_v^k \vee \neg x_w^k) \quad (2b)$$

$$\bigwedge_{(u,v) \in E} (\neg x_u^1 \vee \neg x_v^2) \wedge (\neg x_u^1 \vee \neg x_v^3) \wedge \dots \wedge (\neg x_u^{k-1} \vee \neg x_v^k) \quad (3)$$

polynomial-sized CNF

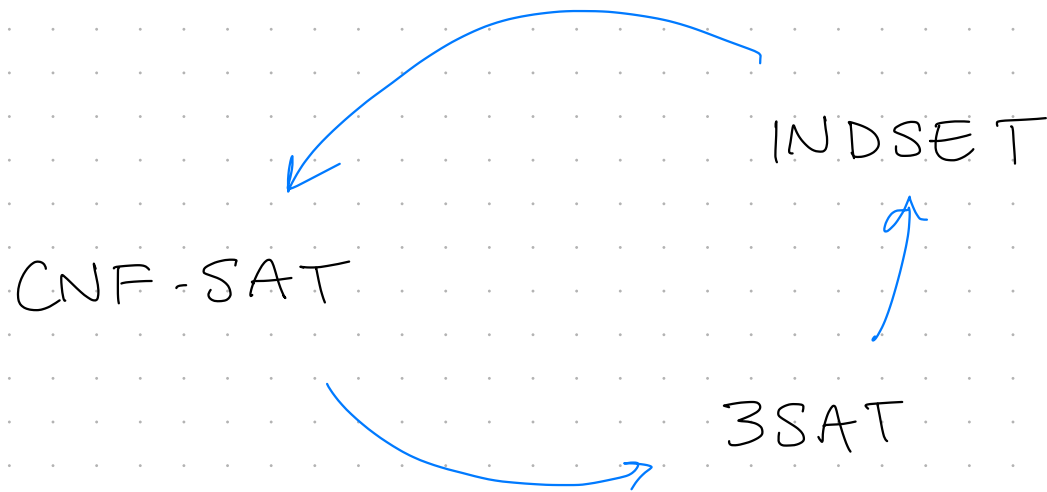
INDSET \leq_p CNF-SAT.

Previously we saw $\exists\text{SAT} \leq_p \text{INDSET}$



INDSET \leq_p CNF-SAT.

Previously we saw $3SAT \leq_p$ INDSET

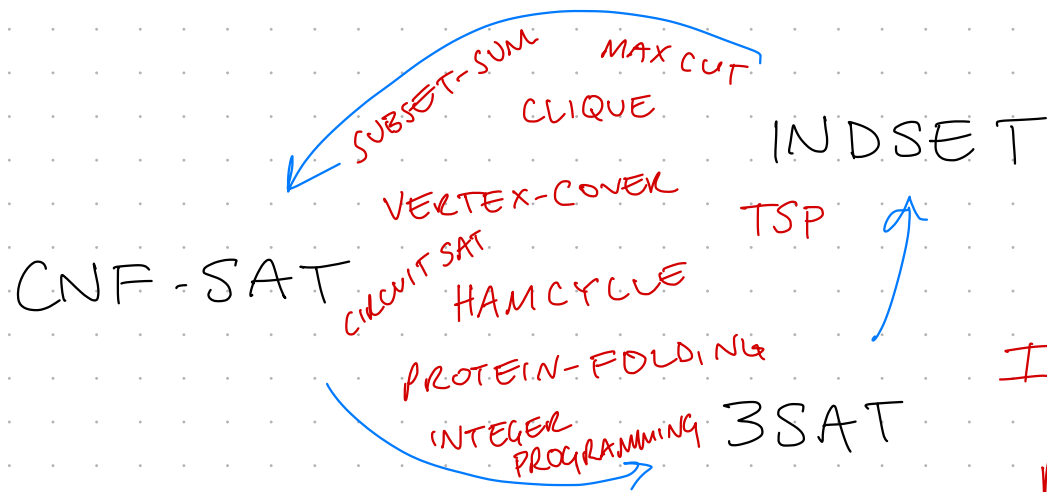


NP-Complete problems form
an equivalence class

(under poly-time Karp reductions)

INDSET \leq_p CNF-SAT.

Previously we saw $3SAT \leq_p$ INDSET



If any NP-Complete problem has a poly-time algorithm, they all do!

(i.e. $P=NP$)

NP-Complete problems form an equivalence class

(under poly-time Karp reductions)

CIRCUIT - SAT \leq_p 3SAT

Given: Logical Circuit $C : \{0,1\}^n \rightarrow \{0,1\}$

Question.

Does there exist $x \in \{0,1\}^n$

s.t. $C(x) = 1$?

CIRCUIT - SAT \leq_p 3SAT

Given: Logical Circuit $C : \{0,1\}^n \rightarrow \{0,1\}$

Question.

Does there exist $x \in \{0,1\}^n$
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Note. CIRCUIT SAT \leq_p 3SAT is a key step in
proof of Cook-Levin Theorem!

Intuition. Logical Circuits can implement any algorithm.

Circuits.

* Represented as a DAG

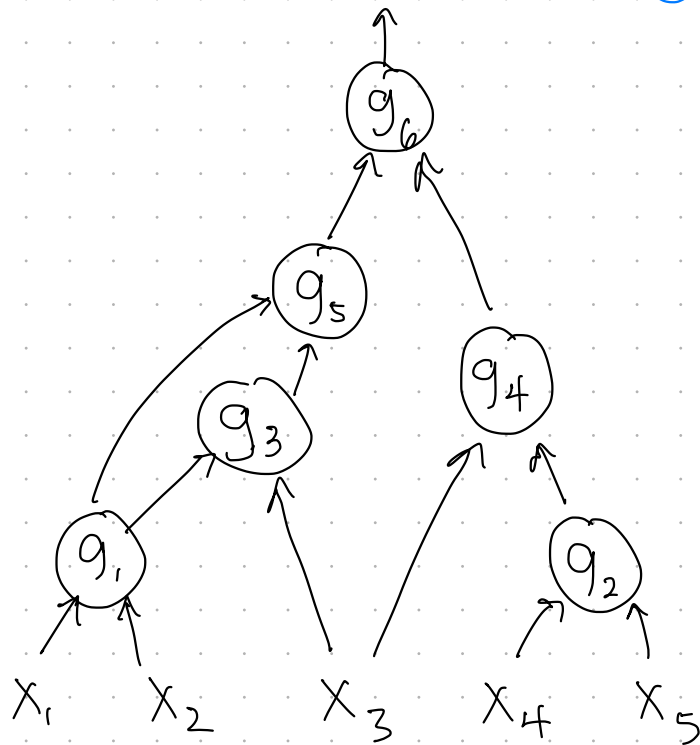
* Vertices \equiv "Gates"

// Each gate computes a boolean fn. on 2-variables

* Edges \equiv "Wires"

→ n total input wires to circuit

→ Output determined by evaluating each gate from bottom to top.



Circuits.

* Represented as a DAG

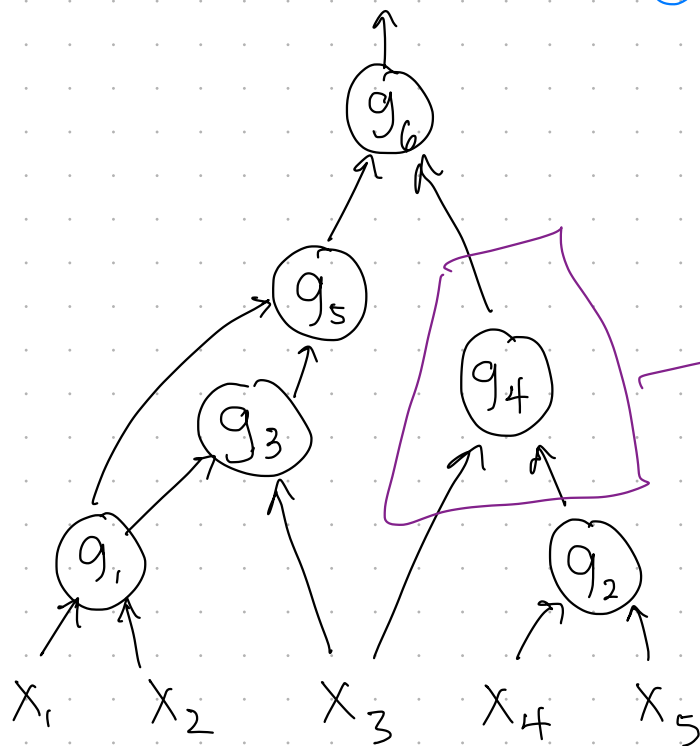
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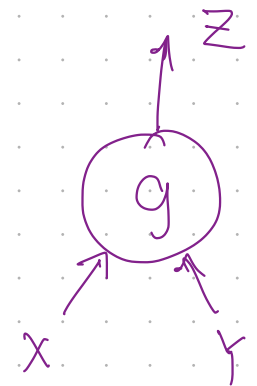
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Reduction idea

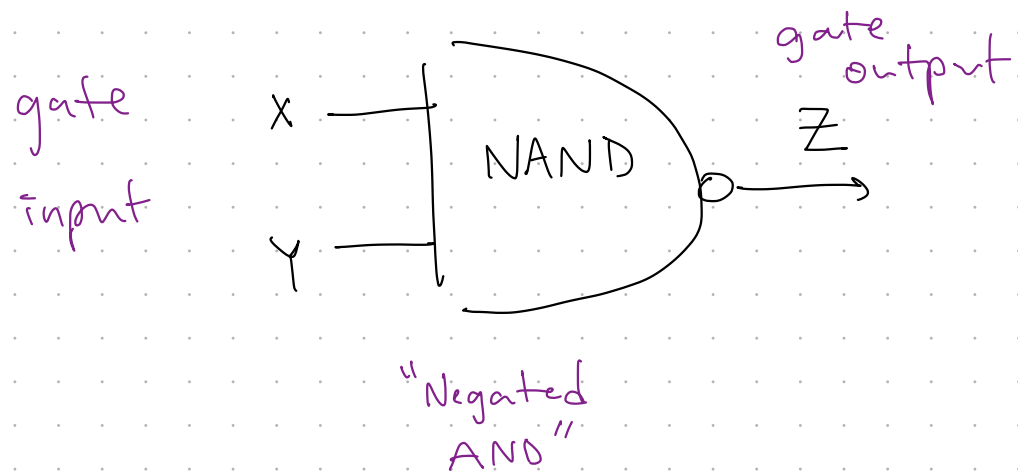
For each gate



Check correctness

$$z \leftrightarrow g(x, y)$$

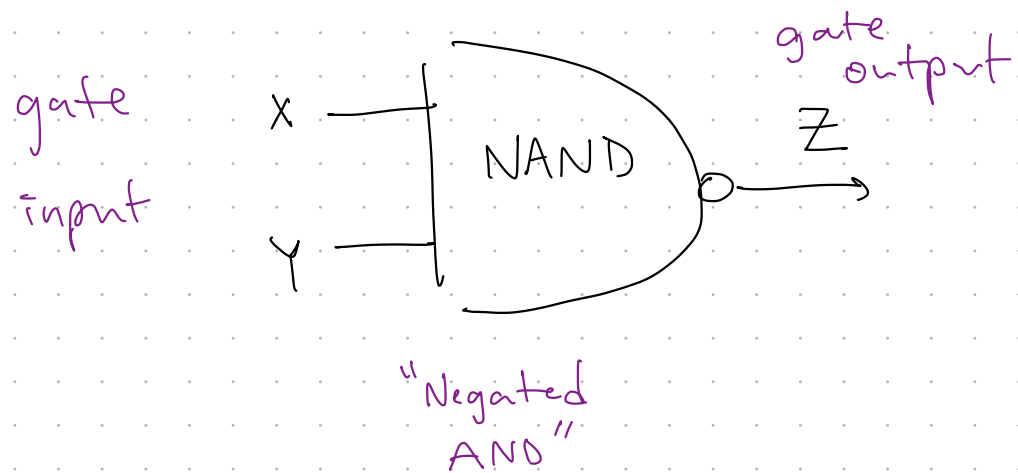
Verify each gate



X	Y	Z = NAND(X, Y)
0	0	1
1	0	1
0	1	1
1	1	0

Note: NAND is Complete

Verify each gate



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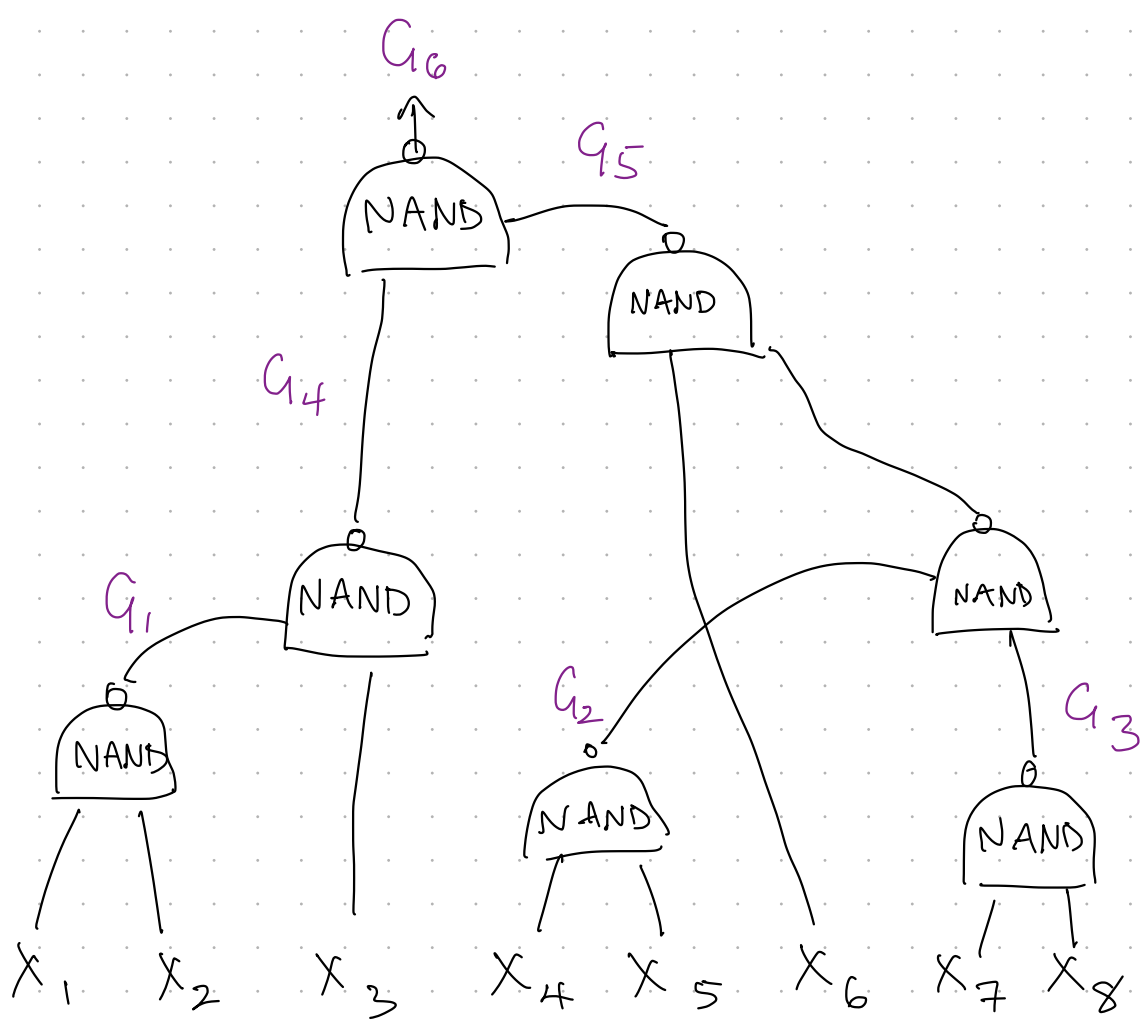
Note: NAND is Complete

GATE GADGET. \rightarrow Set of clause satisfied by any x, y, z s.t. $Z = \text{NAND}(x, y)$

$$Z \leftrightarrow \neg(x \wedge y)$$

$$\Leftrightarrow Z \rightarrow \neg(x \wedge y) \quad \wedge \quad \neg Z \rightarrow (x \wedge y)$$

$$(\neg Z \vee \neg x \vee \neg y) \quad \wedge \quad (Z \vee x) \quad \wedge \quad (Z \vee y)$$



poly-sized CNF
satisfiable iff
 $\exists x \in \{0,1\}^8$ s.t.
 $C(x) = 1$.

$$\varphi_C = G_6 \wedge \left[\begin{array}{l} (\neg G_1 \vee \neg x_1 \vee \neg x_2) \wedge (G_1 \vee x_1) \wedge (G_1 \vee x_2) \quad \text{(gate 1 gadget)} \\ \wedge (\neg G_2 \vee \neg x_4 \vee \neg x_5) \wedge (G_2 \vee x_4) \wedge (G_2 \vee x_5) \quad \text{(gate 2 gadget)} \\ \vdots \\ \wedge (\neg G_6 \vee \neg G_4 \vee \neg G_5) \wedge (G_6 \vee G_4) \wedge (G_6 \vee G_5) \end{array} \right] \text{(gate 6 gadget)}$$