

CS412/CS413

Introduction to Compilers
Tim Teitelbaum

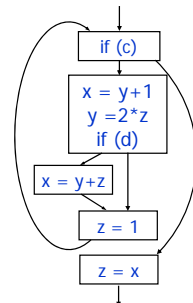
Lecture 25: Liveness and Copy Propagation
28 March 05

Control Flow Graphs

- **Control Flow Graph (CFG)** = graph representation of computation and control flow in the program
 - framework to statically analyze program control-flow
- In a CFG:
 - Nodes are basic blocks; they represent computation
 - Edges characterize control flow between basic blocks
- Can build the CFG representation either from the high IR or from the low IR

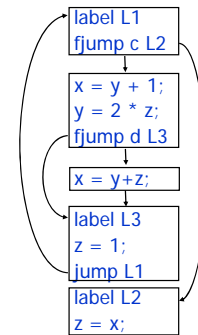
Build CFG from High IR

```
while (c) {  
  x = y + 1;  
  y = 2 * z;  
  if (d) x = y+z;  
  z = 1;  
}  
z = x;
```



Build CFG from Low IR

```
label L1  
fjump c L2  
x = y + 1;  
y = 2 * z;  
fjump d L3  
x = y+z;  
label L3  
z = 1;  
jump L1  
label L2  
z = x;
```

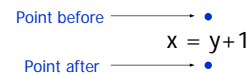


Using CFGs

- **Next:** use CFG representation to statically extract information about the program
 - Reason at compile-time
 - About the run-time values of variables and expressions in all program executions
- **Extracted information example: live variables**
- **Idea:**
 - Define **program points** in the CFG
 - Reason statically about how the information flows between these program points

Program Points

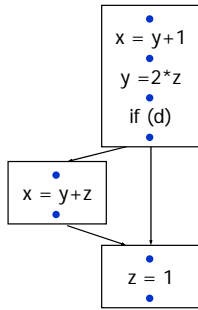
- **Two program points** for each instruction:
 - There is a program point before each instruction
 - There is a program point after each instruction



- In a basic block:
 - Program point after an instruction = program point before the successor instruction

Program Points: Example

- Multiple successor blocks means that point at the end of a block has multiple successor program points
- Depending on the execution, control flows from a program point to one of its successors
- Also multiple predecessors
- How does information propagate between program points?



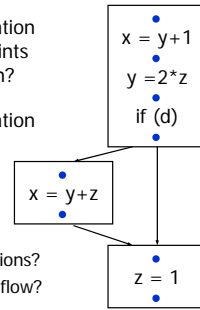
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Flow of Extracted Information

- Question 1: how does information flow between the program points before and after an instruction?
- Question 2: how does information flow between successor and predecessor basic blocks?
- ... in other words:
 - Q1: what is the effect of instructions?
 - Q2: what is the effect of control flow?



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Using CFGs

- To extract information: reason about how it propagates between program points
- Rest of this lecture: how to use CFGs to compute information at each program point for:
 - Live variable analysis, which computes which variables are live at each program point
 - Copy propagation analysis, which computes the variable copies available at each program point

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Live Variable Analysis

- Computes live variables at each program point
 - I.e., variables holding values that may be used later (in some execution of the program)
- For an instruction I , consider:
 - $in[I]$ = live variables at program point before I
 - $out[I]$ = live variables at program point after I
- For a basic block B , consider:
 - $in[B]$ = live variables at beginning of B
 - $out[B]$ = live variables at end of B
- If I = first instruction in B , then $in[B] = in[I]$
- If I' = last instruction in B , then $out[B] = out[I']$

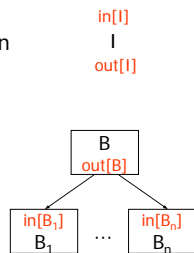
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How to Compute Liveness?

- Answer question 1: for each instruction I , what is the relation between $in[I]$ and $out[I]$?
- Answer question 2: for each basic block B with successor blocks B_1, \dots, B_n , what is the relation between $out[B]$ and $in[B_1], \dots, in[B_n]$?



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Part 1: Analyze Instructions

- Question: what is the relation between sets of live variables before and after an instruction?

- Examples:

conclude	$in[I] = \{y,z\}$	$in[I] = \{y,z,t\}$	$in[I] = \{x,t\}$
	$x = y+z;$	$x = y+z;$	$x = x+1;$
assume	$out[I] = \{z\}$	$out[I] = \{x,t\}$	$out[I] = \{x,t\}$

- ... is there a general rule?

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Analyze Instructions

- **Yes:** knowing variables live after I, can compute variables live before I:
 - All variables live after I are also live before I, unless I defines (writes) them
 - All variables that I uses (reads) are also live before instruction I

$in[I]$
 I
 $out[I]$

- **Mathematically:**

$$in[I] = (out[I] - def[I]) \cup use[I]$$

where:

- $def[I]$ = variables defined (written) by instruction I
- $use[I]$ = variables used (read) by instruction I

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Computing Use/Def

- Compute $use[I]$ and $def[I]$ for each instruction I:
 - if I is $x = y \text{ OP } z$: $use[I] = \{y, z\}$ $def[I] = \{x\}$
 - if I is $x = \text{OP } y$: $use[I] = \{y\}$ $def[I] = \{x\}$
 - if I is $x = y$: $use[I] = \{y\}$ $def[I] = \{x\}$
 - if I is $x = \text{addr } y$: $use[I] = \{\}$ $def[I] = \{x\}$
 - if I is **if** (x) : $use[I] = \{x\}$ $def[I] = \{\}$
 - if I is **return** x : $use[I] = \{x\}$ $def[I] = \{\}$
 - if I is $x = f(y_1, \dots, y_n)$: $use[I] = \{y_1, \dots, y_n\}$
 $def[I] = \{x\}$

(For now, ignore load and store instructions)

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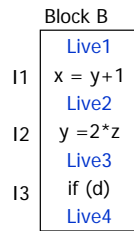
Example

- Example: block B with three instructions I1, I2, I3:

$Live1 = in[B] = in[I1]$
 $Live2 = out[I1] = in[I2]$
 $Live3 = out[I2] = in[I3]$
 $Live4 = out[I3] = out[B]$

- Relation between Live sets:

$Live1 = (Live2 - \{x\}) \cup \{y\}$
 $Live2 = (Live3 - \{y\}) \cup \{z\}$
 $Live3 = (Live4 - \{\}) \cup \{d\}$



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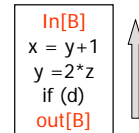
Backward Flow

- **Relation:**

$$in[I] = (out[I] - def[I]) \cup use[I]$$



- **The information flows backward!**
- **Instructions:** can compute $in[I]$ if we know $out[I]$
- **Basic blocks:** information about live variables flows from $out[B]$ to $in[B]$



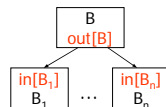
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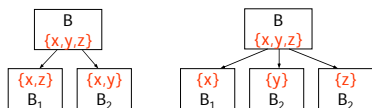
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Part 2: Analyze Control Flow

- **Question:** for each basic block B with successor blocks B_1, \dots, B_n , what is the relation between $out[B]$ and $in[B_1], \dots, in[B_n]$?



- Examples:



- What is the general rule?

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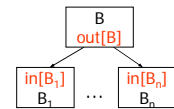
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Analyze Control Flow

- **Rule:** A variables is live at end of block B if it is live at the beginning of **one (or more)** successor blocks
- **Characterizes all possible program executions**

- **Mathematically:**

$$out[B] = \cup_{B' \in succ(B)} in[B']$$



- Again, information flows backward: from successors B' of B to basic block B

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Constraint System

- Put parts together: start with CFG and derive a system of constraints between live variable sets:

$$\begin{cases} \text{in}[I] = (\text{out}[I] - \text{def}[I]) \cup \text{use}[I] & \text{for each instruction } I \\ \text{out}[B] = \bigcup_{B' \in \text{succ}(B)} \text{in}[B'] & \text{for each basic block } B \end{cases}$$

- Solve constraints:
 - Start with empty sets of live variables
 - Iteratively apply constraints
 - Stop when we reach a fixed point

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Constraint Solving Algorithm

for all instructions I do $\text{in}[I] = \text{out}[I] = \emptyset$;

repeat

select an instruction I (or a basic block B) such that

$\text{in}[I] \neq (\text{out}[I] - \text{def}[I]) \cup \text{use}[I]$

or (respectively)

$\text{out}[B] \neq \bigcup_{B' \in \text{succ}(B)} \text{in}[B']$

and update $\text{in}[I]$ (or $\text{out}[B]$) accordingly

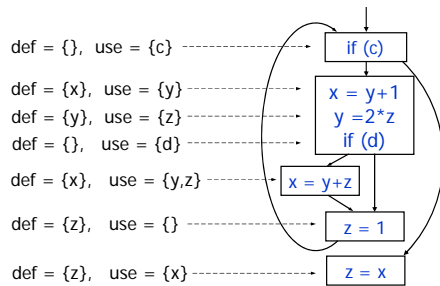
until no such change is possible

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Example

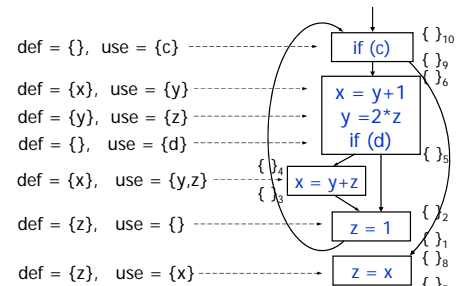


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Example



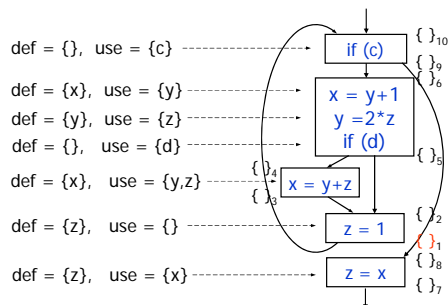
strategy: pick program points in postorder

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Example

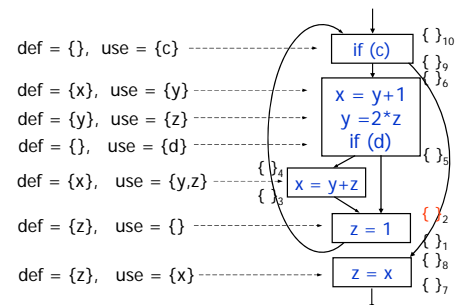


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Example

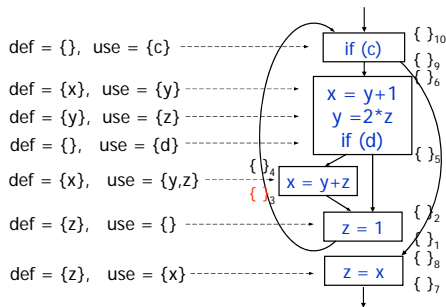


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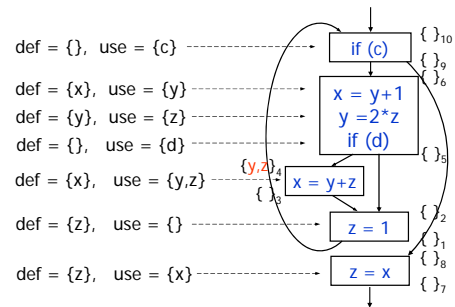
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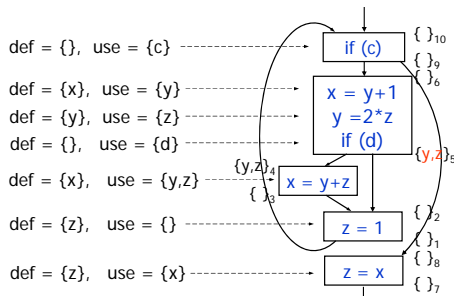
Example



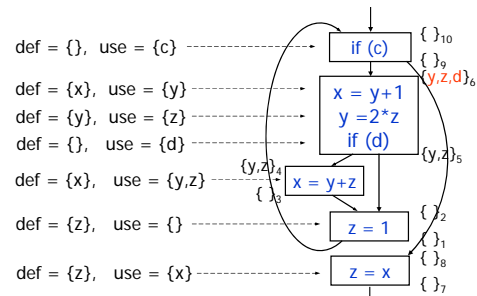
Example



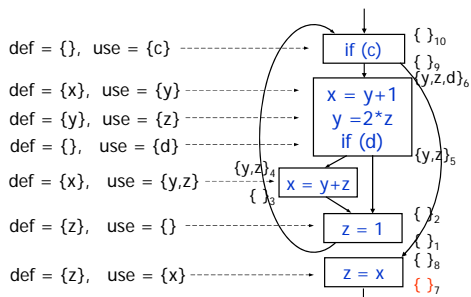
Example



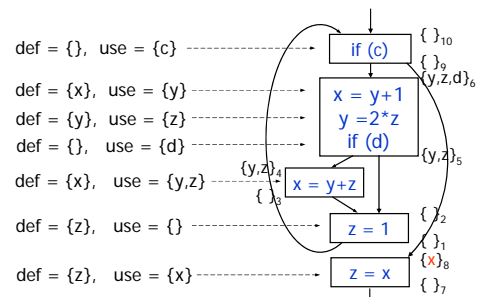
Example



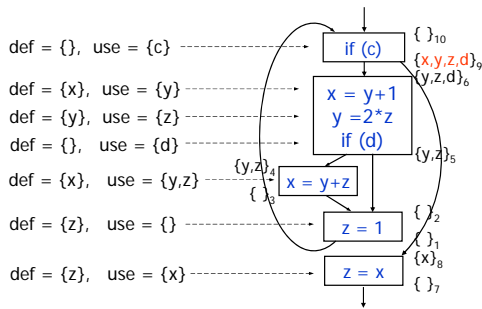
Example



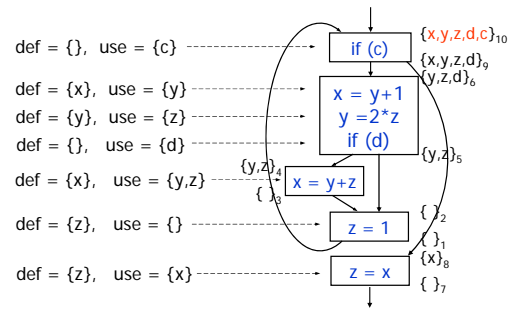
Example



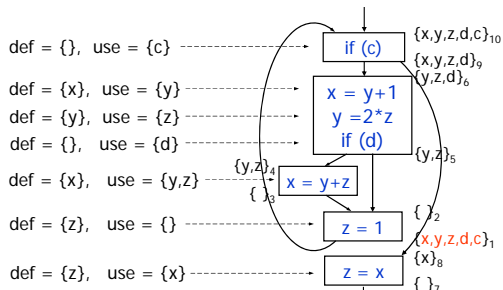
Example



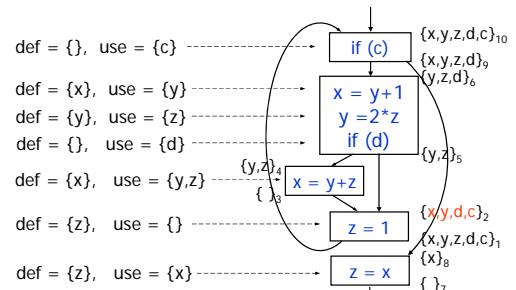
Example



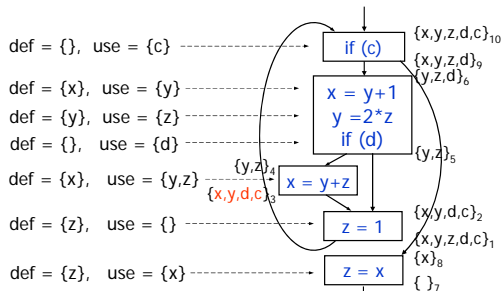
Example



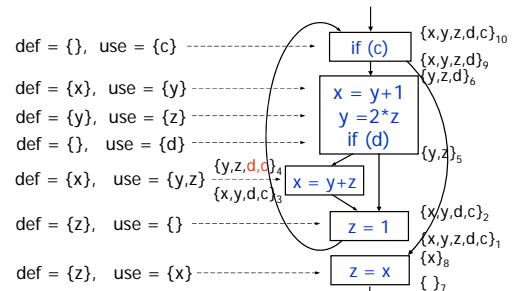
Example



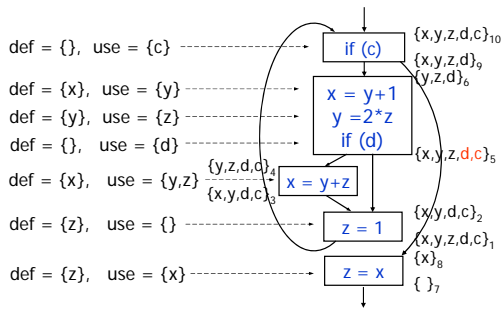
Example



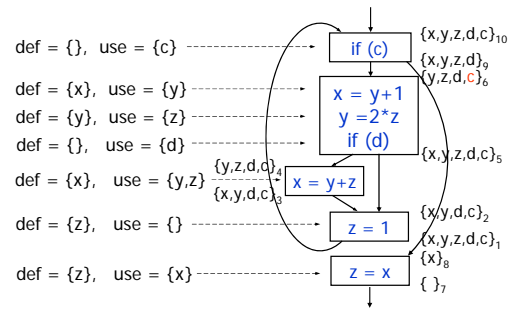
Example



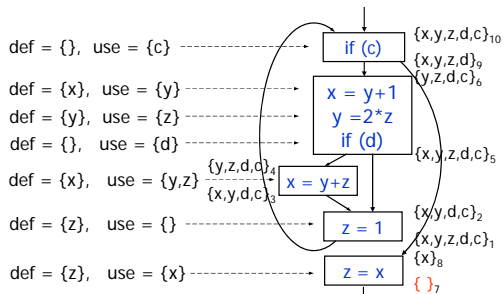
Example



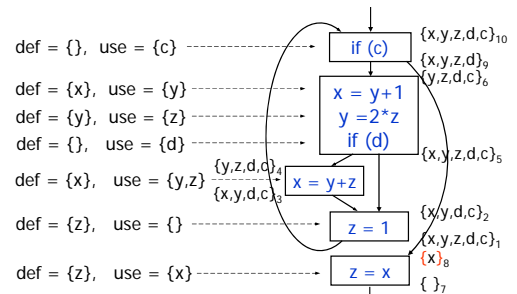
Example



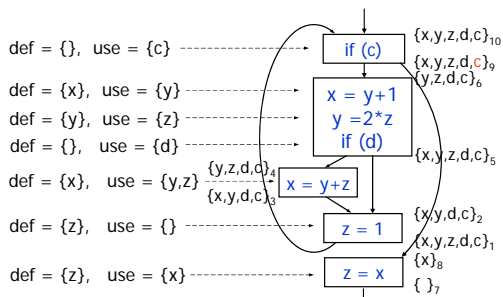
Example



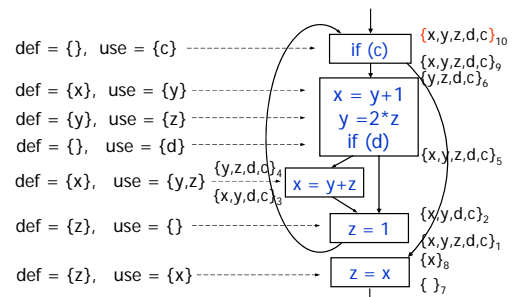
Example



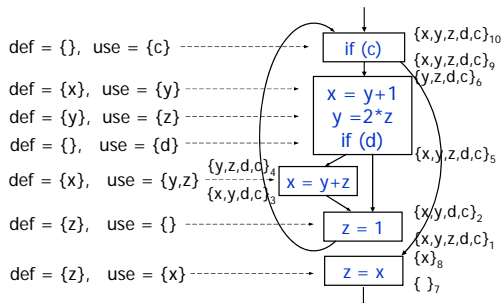
Example



Example



Fixed Point Reached



Copy Propagation

- **Goal:** determine copies available at each program point
- **Information:** set of copies $\langle x=y \rangle$ at each point
- For each instruction I:
 - $\text{in}[I]$ = copies available at program point before I
 - $\text{out}[I]$ = copies available at program point after I
- For each basic block B:
 - $\text{in}[B]$ = copies available at beginning of B
 - $\text{out}[B]$ = copies available at end of B
- If I = first instruction in B, then $\text{in}[B] = \text{in}[I]$
- If I' = last instruction in B, then $\text{out}[B] = \text{out}[I']$

Same Methodology

1. **Express flow of information** (i.e., available copies):
 - For points before and after each instruction ($\text{in}[I]$, $\text{out}[I]$)
 - For points at exit and entry of basic blocks ($\text{in}[B]$, $\text{out}[B]$)
2. **Build constraint system** using the relations between available copies
3. **Solve constraints** to determine available copies at each point in the program

Analyze Instructions

- Knowing $\text{in}[I]$, can compute $\text{out}[I]$:
 - Remove from $\text{in}[I]$ all copies $\langle u=v \rangle$ if variable u or v is written by I
 - Keep all other copies from $\text{in}[I]$
 - If I is of the form $x=y$, add it to $\text{out}[I]$
- **Mathematically:**

$$\text{out}[I] = (\text{in}[I] - \text{kill}[I]) \cup \text{gen}[I]$$
- where:
 - $\text{kill}[I]$ = copies "killed" by instruction I
 - $\text{gen}[I]$ = copies "generated" by instruction I



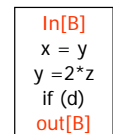
Computing Kill/Gen

- Compute $\text{kill}[I]$ and $\text{gen}[I]$ for each instruction I:
- | | | |
|------------------------------------|---------------------------|---|
| if I is $x = y \text{ OP } z$: | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{u=v \mid u \text{ or } v \text{ is } x\}$ |
| if I is $x = \text{OP } y$: | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{u=v \mid u \text{ or } v \text{ is } x\}$ |
| if I is $x = y$: | $\text{gen}[I] = \{x=y\}$ | $\text{kill}[I] = \{u=v \mid u \text{ or } v \text{ is } x\}$ |
| if I is $x = \text{addr } y$: | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{u=v \mid u \text{ or } v \text{ is } x\}$ |
| if I is if (x) : | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{\}$ |
| if I is return x : | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{\}$ |
| if I is $x = f(y_1, \dots, y_n)$: | $\text{gen}[I] = \{\}$ | $\text{kill}[I] = \{u=v \mid u \text{ or } v \text{ is } x\}$ |
- (again, ignore load and store instructions)

Forward Flow

- **Relation:**

$$\text{out}[I] = (\text{in}[I] - \text{kill}[I]) \cup \text{gen}[I]$$
- **The information flows forward!**
- **Instructions:** can compute $\text{out}[I]$ if we know $\text{in}[I]$
- **Basic blocks:** information about available copies flows from $\text{in}[B]$ to $\text{out}[B]$



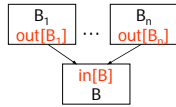
Analyze Control Flow

- **Rule:** A copy is available at beginning of block B if it is available at the end of **all** predecessor blocks
- Characterizes all possible program executions

- Mathematically:

$$\text{in}[B] = \bigcap_{B' \in \text{pred}(B)} \text{out}[B']$$

$$B' \in \text{pred}(B)$$



- Information flows forward: from predecessors B' of B to basic block B

Constraint System

- **Build constraints:** start with CFG and derive a system of constraints between sets of available copies:

$$\begin{cases} \text{out}[I] = (\text{in}[I] - \text{kill}[I]) \cup \text{gen}[I] & \text{for each instruction } I \\ \text{in}[B] = \bigcap_{B' \in \text{pred}(B)} \text{out}[B'] & \text{for each basic block } B \end{cases}$$

- **Solve constraints:**

- Start with empty set of available copies at start and universal set of available copies everywhere else
- Iteratively apply constraints
- Stop when we reach a fixed point

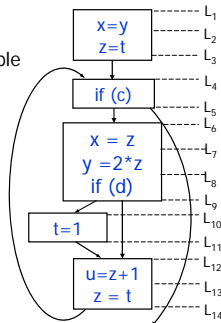
Example

- What are the available copies at the end of the program?

x=y?

z=t?

x=z?



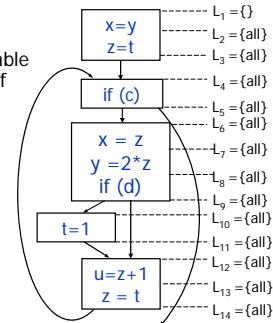
Example

- What are the available copies at the end of the program?

x=y?

z=t?

x=z?



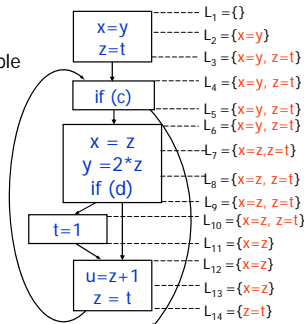
Iteration 1

- What are the available copies at the end of the program?

x=y?

z=t?

x=z?



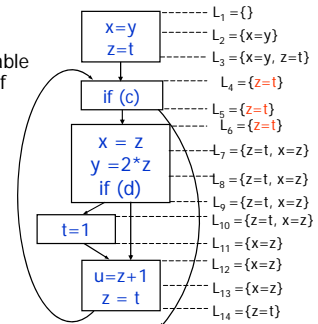
Iteration 2

- What are the available copies at the end of the program?

x=y?

z=t?

x=z?



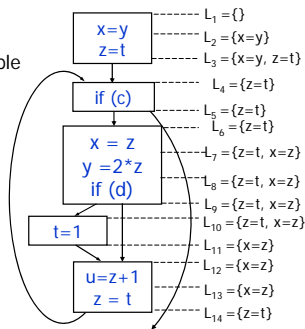
Fixed Point Reached!

- What are the available copies at the end of the program?

$x=y$? NO

$z=t$? YES

$x=z$? NO



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Summary

- Extracting information about live variables and available copies is similar
 - Define the required information
 - Define information before/after instructions
 - Define information at entry/exit of blocks
 - Build constraints for instructions/control flow
 - Solve constraints to get needed information
- ...is there a general framework?
 - Yes: dataflow analysis!

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