

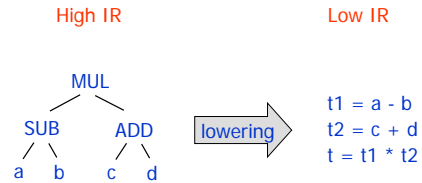
## CS412/CS413

Introduction to Compilers  
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Lecture 19: Efficient IL Lowering  
7 March 07

## IR Lowering

- Use temporary variables for the translation
- Temporary variables in the Low IR store intermediate values corresponding to the nodes in the High IR



## Lowering Methodology

- Define simple translation rules for each High IR node
  - Arithmetic:  $e1 + e2$ ,  $e1 - e2$ , etc.
  - Logic:  $e1 \text{ AND } e2$ ,  $e1 \text{ OR } e2$ , etc.
  - Array access expressions:  $e1[e2]$
  - Statements:  $\text{if } (e) \text{ then } s1 \text{ else } s2$ ,  $\text{while } (e) \text{ } s1$ , etc.
  - Function calls  $f(e1, \dots, eN)$
- Recursively traverse the High IR trees and apply the translation rules
- Can handle nested expressions and statements

## Notation

- Use the following notation:
  - $T[e]$  = the low-level IR representation of high-level IR construct  $e$
  - $T[e]$  is a sequence of Low-level IR instructions
- If  $e$  is an expression (or a statement expression), it represents a value
- Denote by  $t = T[e]$  the low-level IR representation of  $e$ , whose result value is stored in  $t$
- For variable  $v$ :  $t = T[v]$  is the copy instruction  $t = v$

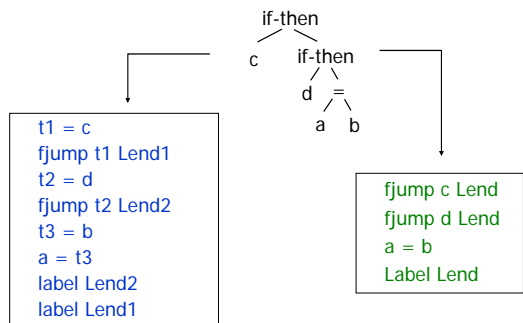
## Nested Expressions

- In these translations, expressions may be nested;
  - Translation recurses on the expression structure
  - Example:  $t = T[(a - b) * (c + d)]$ 
    - $t1 = a$
    - $t2 = b$
    - $t3 = t1 - t2$
    - $t4 = b$
    - $t5 = c$
    - $t5 = t4 + t5$
    - $t = t3 * t5$
- $T[(a - b)]$
- $T[(c + d)]$
- $T[(a - b) * (c + d)]$

## Nested Statements

- Same for statements: recursive translation
  - Example:  $T[\text{if } c \text{ then if } d \text{ then } a = b]$ 
    - $t1 = c$
    - $\text{fjump } t1 \text{ Lend1}$
    - $t2 = d$
    - $\text{fjump } t2 \text{ Lend2}$
    - $t3 = b$
    - $a = t3$
    - $\text{label Lend2}$
    - $\text{label Lend1}$
- $T[a = b]$
- $T[\text{if } d \dots]$
- $T[\text{if } c \text{ then } \dots]$

## IR Lowering Efficiency



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## Efficient Lowering Techniques

- How to generate efficient Low IR:
  1. Reduce number of temporaries
    - a) Don't use temporaries that duplicate variables
    - b) Use "accumulator" temporaries
    - c) Reuse temporaries in Low IR
  2. Don't generate multiple adjacent label instructions
  3. Encode conditional expressions in control flow
  4. Eliminate jumps to unconditional jumps

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## No Duplicated Variables

- Basic algorithm:
  - Translation rules recursively traverse expressions until they reach terminals (variables and numbers)
  - Then translate  $t = T[v]$  into  $t = v$  for variables
  - And translate  $t = T[n]$  into  $t = n$  for constants
- Better:
  - terminate recursion one level before terminals
  - Need to check at each step if expressions are terminals
  - Recursively generate code for children only if they are non-terminal expressions

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## No Duplicated Variables

- $t = T[e1 \text{ OP } e2]$ 
  - $t1 = T[e1]$ , if  $e1$  is not terminal
  - $t2 = T[e2]$ , if  $e2$  is not terminal
  - $t = x1 \text{ OP } x2$
- where:
  - $x1 = t1$ , if  $e1$  is not terminal
  - $x1 = e1$ , if  $e1$  is terminal
  - $x2 = t2$ , if  $e2$  is not terminal
  - $x2 = e2$ , if  $e2$  is terminal
- Similar translation for statements with conditional expressions: if, while, switch

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## Example

- $t = T[(a+b)*c]$
- Operand  $e1 = a+b$ , is not terminal
- Operand  $e2 = c$ , is terminal
- Translation:  $t1 = T[e1]$   
 $t = t1 * c$
- Recursively generate code for  $t1 = T[e1]$
- For  $e1 = a+b$ , both operands are terminals
- Code for  $t1 = T[e1]$  is  $t1 = a+b$
- Final result:  $t1 = a + b$   
 $t = t1 * c$

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## Accumulator Temporaries

- Use the same temporary variables for operands and result
- Translate  $t = T[e1 \text{ OP } e2]$  as:
  - $t = T[e1]$
  - $t1 = T[e2]$
  - $t = t \text{ OP } t1$
- Example:  $t = T[(a+b)*c]$ 
  - $t = a + b$
  - $t = t * c$

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## Reuse Temporaries

- **Idea:** in the translation of  $t = T[ e1 \text{ OP } e2 ]$  as:
  - $t = T[ e1 ]$ ,  $t' = T[ e2 ]$ ,  $t = t \text{ OP } t'$
 temporary variables from the translation of  $e1$  can be reused in the translation of  $e2$
- **Observation:** temporary variables compute intermediate values, so they have limited lifetime
- **Algorithm:**
  - Use a stack of temporaries
  - This corresponds to the stack of the recursive invocations of the translation functions  $t = T[ e ]$
  - All the temporaries on the stack are alive

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## Reuse Temporaries

- **Implementation:** use counter  $c$  to implement the stack
  - Temporaries  $t(0), \dots, t(c)$  are alive
  - Temporaries  $t(c+1), t(c+2), \dots$  can be reused
  - Push means increment  $c$ , pop means decrement  $c$
- In the translation of  $t(c) = T[ e1 \text{ OP } e2 ]$ 
  - $t(c) = T[ e1 ]$   
.....  $c = c+1$
  - $t(c) = T[ e2 ]$   
.....  $c = c-1$
  - $t(c) = t(c) \text{ OP } t(c+1)$

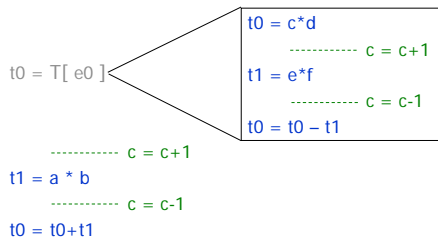
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## Example

- $t0 = T[ ((c*d) - (e*f)) + (a*b) ]$   
.....  $c = 0$



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## Trade-offs

- **Benefits of fewer temporaries:**
  - Smaller symbol tables
  - Smaller analysis information propagated during dataflow analysis
- **Drawbacks:**
  - Same temporaries store multiple values
  - Some analysis results may be less precise
  - Also harder to reconstruct expression trees (more convenient for instruction selection)
- **Possible compromise:**
  - Different temporaries for intermediate values in each statement
  - Reuse temporaries for different statements

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## No Adjacent Labels

- Translation of control flow constructs (if, while, switch) and short-circuit conditionals generates label instructions
- Nested if/while/switch statements and nested short-circuit AND/OR expressions may generate adjacent labels
- Simple solution: have a second pass that merges adjacent labels
  - And a third pass to adjust the branch instructions
- More efficient: **backpatching**
  - Directly generate code without adjacent label instructions
  - Code has placeholders for jump labels, fill in labels later

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## Encode Booleans in Control-Flow

- Consider  $T[ \text{if } (a < b \text{ SC-AND } c < d) \text{ then } x = y; ]$ 
  - ```

t = a < b
fjump t L1
t = c < d
label L1
fjump t L2
x = y
label L2
                    
```
  - Condition:  $t = a < b \text{ SC-AND } c < d$
  - Control flow:  $\text{if } (t) \ x = y$
- ... can we do better?

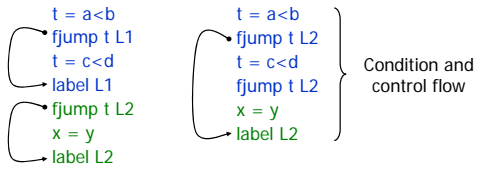
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## Encode Booleans in Control-Flow

- Consider  $T[\text{if } (a < b \text{ SC-AND } c < d) \text{ then } x = y; ]$



- If  $t = a < b$  is false, program branches to label L2

## How It Works

- For each boolean expression  $e$ , and  $b$  either true or false:

$T[e, L, b]$

is the code that computes  $e$  and branches to  $L$  if  $e$  evaluates to  $b$ , and falls through to the next sequential instruction on  $!b$

- Must redefine:

$T[s]$  for if, while statements

## Define New Translations

- $T[\text{if}(e) \text{ then } s1 \text{ else } s2]$ 

```

T[ e, L, false ]
T[ s1 ]
jump Lend
label L
T[ s2 ]
label Lend

```
- $T[\text{if}(e) \text{ then } s]$ 

```

T[ e, L, false ]
T[ s ]
label L

```

## While Statement

- $T[\text{while } (e) \text{ s}]$ 

```

label Ltest
T[ e, L, false ]
T[ s ]
jump Ltest
label L

```

## SC-Boolean Expression Translations

- $T[v, L, b] : \text{if } b \text{ then } \text{tjump } v, L \text{ else } \text{fjump } v, L$
- $T[!e, L, b] : T[e, L, !b]$
- $T[e1 \text{ SC-OR } e2, L, true]$ 

```

T[ e1, L, true ]
T[ e2, L, true ]

```
- $T[e1 \text{ SC-AND } e2, L, false]$ 

```

T[ e1, L, false ]
T[ e2, L, false ]

```
- $T[e1 \text{ SC-OR } e2, L, false]$ 

```

T[ e1, Lnext, true ]
T[ e2, L, false ]
label Lnext

```
- $T[e1 \text{ SC-AND } e2, L, true]$ 

```

T[ e1, Lnext, false ]
T[ e2, L, true ]
label Lnext

```

## Eliminate Jumps to Unconditional Jumps

- Example
 

```

T[ if a then if b then c=d else e=f else g=h ]
fjump a L1
fjump b L2
c = d
jump Lend2
label L2
e = f
label Lend2
jump Lend1
label L1
g = h
label Lend1

```

## Eliminate Jumps to Unconditional Jumps

- Example  
 T[ if a then if b then c=d else e=f else g=h]  
 fjump a L1  
   fjump b L2  
     c = d  
     jump Lend1  
   label L2  
     e = f  
   jump Lend1  
   label L1  
     g = h  
   label Lend1

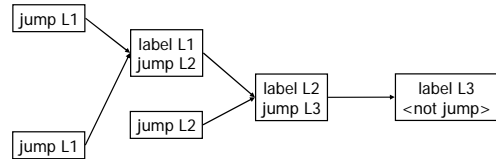
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## Eliminate Jumps to Unconditional Jumps

- Each set of jumps to jumps that end in the same label form a tree (with the ultimate label as root)
- Traverse tree and retarget all jumps to the root label



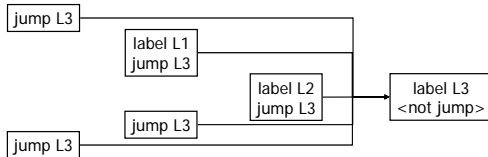
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## Eliminate Jumps to Jumps

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