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Introduction to Compilers Radu Rugina

Lecture 23: Live Variable Analysis 26 Mar 06

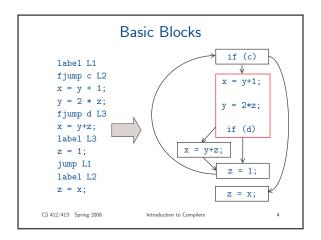
Control Flow Graphs

- Control Flow Graph (CFG) = graph representation of computation and control flow in the program
 - framework to statically analyze program control-flow
- In a CFG:
 - Nodes are basic blocks; they represent computation
 - Edges characterize control flow between basic blocks
- Can build the CFG representation either from the high IR or from the low IR

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From Three-Address Code to CFG label L1 fjump c L2 x = y+1;x = y + 1;y = 2 * z;y = 2*z;fjump d L3 x = y+z;if (d) label L3 z = 1;jump L1 z = 1;label L2 z = x;z = x; CS 412/413 Spring 2006 Introduction to Compilers



Using CFGs

- Next: use CFG representation to statically extract information about the program
 - Reason at compile-time
 - About the run-time values of variables and expressions in all program executions
- Extracted information example: live variables
- Idea:
 - Define program points in the CFG
 - Reason statically about how the information flows between these program points

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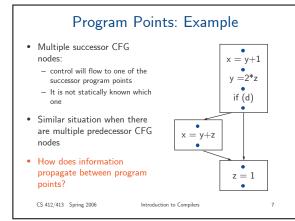
Program Points

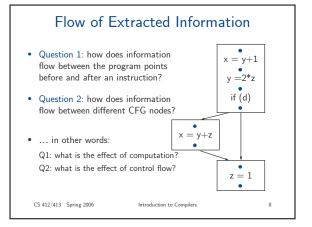
- Two program points for each instruction:
 - There is a program point before each instruction
 - There is a program point after each instruction

Point before x = y+1Point after y+1

- In a basic block:
 - Program point after an instruction = program point before the successor instruction

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Using CFGs

- To extract information: reason about how it propagates between program points
- Rest of this lecture: how to use CFGs to compute information at each program point for:
 - Live variable analysis, which computes live variables are live at each program point
 - Copy propagation analysis, which computes the variable copies available at each program point

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Live Variable Analysis

- Using the CFG structure to perform live variable analysis
 - A variable is live if its value might be needed later
 - Goal: compute all live variables at each program point
- For each CFG node n, consider:
 - -in[n] = live variables at program point before n
 - $\mathtt{out[n]} = \mathsf{live}$ variables at program point after n
- CFG node can be either:
 - An instruction I
 - A basic block B

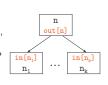
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How to Compute Liveness?

- Answer question 1: for each CFG node n, what is the relation between in[n] and out [n] ?
- Answer question 2: for each CFG node n with successors n₁, ..., n_k, what is the relation between out[n] and in[n₁], ..., in[n_k]?



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in[n]

out[n]

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Part 1: Analyze Instructions

- Question: what is the relation between sets of live variables before and after a node?
 in[n]
 out[n]
- Examples:

• ... is there a general rule?

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Analyze Instructions

in[n]

out[n]

- Yes: knowing variables live after n, we can compute variables live before n:
 - All variables live after n are also live before n, unless n defines (writes) them
 - All variables that n uses (reads) are also live before instruction n
- Mathematically:

```
in[n] = (out[n] - def[n]) \cup use[n]
```

where:

- def [n] = variables defined (written) by node n
- use [n] = variables used (read) by node n

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Computing Use/Def

• Compute use[n] and def[n] for each instruction:

(For now, ignore load and store instructions)

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Example

• Example: three consecutive instructions

I1, I2, I3:

Live2 = (Live3- $\{y\}$) U $\{z\}$

Live3 = (Live4- $\{\}$) U $\{d\}$

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Backward Flow

• Relation:

$$in[I] = (out[I] - def[I]) \cup use[I]$$

- Can compute in[I] if we know out[I]
- The information flows backward

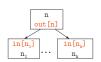
in[I]
I
out[I]

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Part 2: Analyze Control Flow

 Question: for each node n with successor blocks n₁, ..., n_k, what is the relation between out [n] and in [n₁], ..., in [n_k]?



Live4

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Examples:



• What is the general rule?

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Analyze Control Flow

- Rule: A variables is live at a program point if it is live at one of the successor points
- Characterizes all possible program executions



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• Again, information flows backward: from successors n' of n, to n itself

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Constraint System

• Put parts together: start with CFG and derive a system of constraints between live variable sets:

```
 \begin{cases} \text{in}[n] = ( \ \text{out}[n] - \text{def}[n] \ ) \ \text{$U$ use}[n] \ \text{for each node $n$} \\ \text{out}[n] = \bigcup_{\substack{n' \in \text{succ}(n)}} \text{in}[n'] \end{cases}
```

- Solve constraints:
 - Start with empty sets of live variables
 - Iteratively apply constraints
 - Stop when we reach a fixed point

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Constraint Solving Algorithm

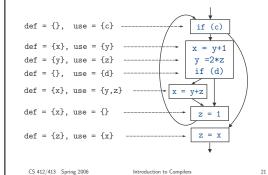
```
For all CFG nodes n: in[n] = out[n] = \emptyset
worklist = all "return" (exit) nodes

while (worklist is not empty)
remove n from worklist
in[n] = ( out[n] - def[n] ) \cup use[n]

for each predecessor n':
out[n'] = out[n'] \cup in[n]
if (out[n'] changed)
add n' to worklist

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```

Example: Live Variable Analysis



Copy Propagation

- Goal: determine copies available at each program point
- Information: set of copies <x=y> at each point
- For each CFG node:
 - $-\inf[n] = \text{copies available at program point before } n$
 - out[n] = copies available at program point after n

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Copy Propagation Analysis

- Knowing in[n], we can compute out[n]:
 - Remove from in[n] all copies <u=v> if variable u or v is written by n

- Keep all other copies from in[n]

- If n is of the form x=y, add <x=y> to out [n]

 A copy is available at point before n if it is available at the end of all predecessor program points

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out[n]

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Example: Copy Propagation

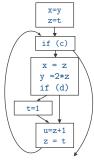
 What are the available copies at the end of the program?

> x=y? z=t?

v=7

X=Z

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Summary

- Extracting information about live variables, available copies:
 - Define the required information
 - Build constraints for instructions/control flow
 - Solve constraints to get needed information
- ...is there a general framework?
 - Yes: dataflow analysis!

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