CS412/413

Introduction to Compilers Radu Rugina

Lecture 18: Activation Records 03 Mar 06

Where We Are Source code -Lexical, Syntax, and if (b == 0) a = b; Semantic Analysis IR Generation Three-address code Optimizations Optimized Three-address code Assembly code Assembly code generation cmp \$0,%ecx cmovz %eax,%edx CS 412/413 Spring 2006

Assembly vs. IR

- · Assembly code:
 - Finite set of registers
 - Variables = memory locations (no names)
 - Variables accessed differently: global, local, heap, args, etc.
 - Uses a run-time stack (with special instructions)
 - Calling sequences: special sequences of instructions for function calls and returns
 - Instruction set of target machine
- Low IR code:
 - Variables (and temporaries)
 - No run-time stack
 - No calling sequences
 - Some abstract set of instructions

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IR to Assembly Translation

- Calling sequences:
 - Translate function calls and returns into appropriate sequences which: pass parameters, save registers, and give back return values
 - Consists of push/pop operations on the run-time stack
- - Translate accesses to specific kinds of variables (globals, locals, arguments, etc)
 - Register Allocation: map the variables to registers
- Instruction set:
 - Account for differences in the instruction set
 - Instruction selection: map sets of low level IR instructions to instructions in the target machine

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x86 Quick Overview

- · Few registers:
 - General purpose 32bit: eax, ebx, ecx, edx, esi, edi
 - Also 16-bit: ax, bx, etc., and 8-bit: al, ah, bl, bh, etc.
 - Stack registers: esp, ebp
- · Many instructions:
 - Arithmetic: add, sub, inc, mod, idiv, imul, etc.
 - Logic: and, or, not, xor
 - Comparison: cmp, test
 - Control flow: jmp, jcc, jeczFunction calls: call, ret

 - Data movement: mov (many variants)
 - Stack manipulations: push, pop
 - Other: lea

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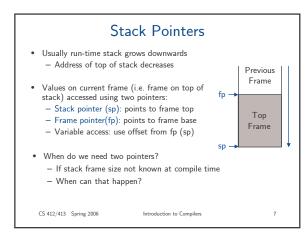
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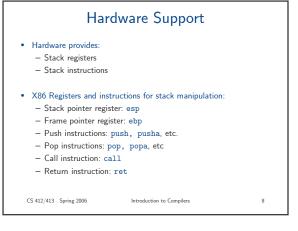
Run-Time Stack

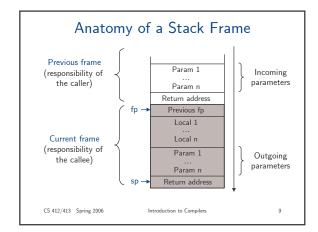
- A frame (or activation record) for each function execution
 - Represents execution environment of the function
 - Includes: local variables, parameters, return value, etc.
 - Different frames for recursive function invocations
- Run-time stack of frames:
 - Push f's frame on stack when program calls f
 - Pop stack frame when f returns
 - Top frame = frame of currently executed function
- This mechanism is necessary to support recursion
 - Different activations of the same recursive function have different stack frames

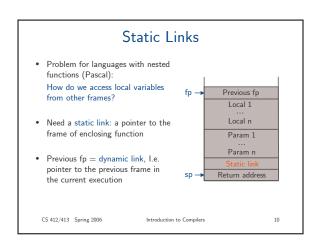
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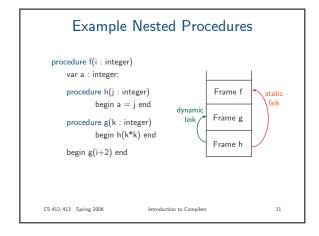
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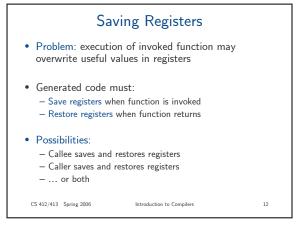












Calling Sequences

- How to generate the code that builds the frames?
- Generate code which pushes values on stack:
 - 1. Before call instructions (caller responsibilities)
 - 2. At function entry (callee responsibilities)
- Generate code which pops values from stack:
 - 3. After call instructions (caller responsibilities)
 - 4. At return instructions (callee responsibilities)
- Calling sequences = sequences of instructions performed in each of the above 4 cases

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Push Values on Stack

- Code before call instruction:
 - Push caller-saved registers
 - Push each actual parameter (in reverse order)
 - Push static link (if necessary)
 - Push return address (current program counter) and jump to caller code
- Prologue = code at function entry
 - Push dynamic link (i.e. current fp)
 - Old stack pointer becomes new frame pointer
 - Push local variables
 - Push callee-saved registers

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Pop Values from Stack

- Epilogue = code at return instruction
 - Pop (restore) callee-saved registers
 - Restore old stack pointer (pop callee frame!)
 - Pop old frame pointer
 - Pop return address and jump to that address
- Code after call
 - Pop (restore) caller-saved registers
 - Pop parameters from the stack
 - Use return value

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Example: Pentium

- Consider call foo(3, 5), %ecx caller-saved, %ebx callee-saved, no static links, result passed back in %eax
- Code before call instruction:

• Prologue:

```
push %ebp // push old fp
mov %esp, %ebp // compute new fp
sub $12, %esp // push 3 integer local variables
push %ebx // push callee saved registers
```

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Example: Pentium

• Epilogue:

• Code after call instruction:

```
add $8,%esp // pop parameters
pop %ecx // restore caller-saved registers
```

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Accessing Stack Variables • To access stack variables: use offsets from fp Param n Param 1 Example: Return address 8(%ebp) = parameter 1ebp -12(%ebp) = parameter 2 ebp-4 --4(%ebp) = local 1· Translate low-level code to take Param 1 into account the frame pointer: Param n a = p+1Return address -4(%ebp) = 16(%ebp)+1CS 412/413 Spring 2006 Introduction to Compilers 18

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Accessing Other Variables

• Global variables

- Are statically allocated
- Their addresses can be statically computed
- Don't need to translate low $\ensuremath{\mathsf{IR}}$

• Heap variables

- Are unnamed locations
- Can be accessed only by dereferencing variables which hold their addresses
- Therefore, they don't explicitly occur in low-level code

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